

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**

## Refine Search

### Search Results -

Term	Documents
(3 AND 5).PGPB,USPT.	4
(L3 AND L5).PGPB,USPT.	4

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L3 and l5

Refine Search

Recall Text

Clear

Interrupt

### Search History

 DATE: Tuesday, August 03, 2004    [Printable Copy](#)    [Create Case](#)

Set  
 Name  
 side by  
 side

Query

Hit  
 Count

Set  
 Name  
 result set

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L19</u>	L3 and l5	4	<u>L19</u>
<u>L18</u>	L3 and l4	4	<u>L18</u>
<u>L17</u>	L9 and l2	5	<u>L17</u>
<u>L16</u>	L8 and l2	6	<u>L16</u>
<u>L15</u>	L6 and l2	22	<u>L15</u>
<u>L14</u>	L5 and l2	7	<u>L14</u>
<u>L13</u>	L5 and l1	145	<u>L13</u>
<u>L12</u>	L6 and l1	264	<u>L12</u>
<u>L11</u>	L8 and l1	126	<u>L11</u>
<u>L10</u>	L9 and l1	52	<u>L10</u>
<u>L9</u>	(712/227, 202, 248)[CCLS]	477	<u>L9</u>
<u>L8</u>	(712/3-300)[CCLS]	10066	<u>L8</u>

<u>L7</u>	(712/3-300)![CCLS]	10066	<u>L7</u>
<u>L6</u>	(714/15-57)![CCLS]	8659	<u>L6</u>
<u>L5</u>	(717/120-161)[CCLS]	4086	<u>L5</u>
<u>L4</u>	(717/120-161)![CCLS]	4086	<u>L4</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L3</u>	debug\$5 near15 (host\$3 or master) and (break or break near1 point\$1) and reset\$6 and stack near12 debug\$5	22	<u>L3</u>
<u>L2</u>	debug\$5 near15 (host\$3 or master) and (break or break near1 point\$1) near15 reset\$6	38	<u>L2</u>
<u>L1</u>	debug\$5 near15 (host\$3 or master)	1365	<u>L1</u>

END OF SEARCH HISTORY



**IEEE Xplore®**  
RELEASE 1.8

Welcome  
**United States Patent and Trademark Office**

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)

## Quick Links

» **Se**

**Welcome to IEEE Xplore®**

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

## Member Services

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

**IEEE Enterprise**

-  Access the  
IEEE Enterprise  
File Cabinet

 **Print Format**

Your search matched **13** of **1058483** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

### Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

debug and (break or breakpoint)

## Search

☐ Check to search within this result set

### Results Key:

**JNL** = Journal or Magazine    **CNF** = Conference    **STD** = Standard

### 1 Breakpoint debugging mechanism for microprocessor design

Wang Danghui; Gao Deyuan; Li Tao;

ASIC, 2003. Proceedings. 5th International Conference on , Volume: 1 , 21-2  
2003

Pages:456 - 458 Vol.1

[\[Abstract\]](#)   [\[PDF Full-Text \(285 KB\)\]](#)   **IEEE CNF**

## 2 A knowledge base approach to the specification of real time system requirements

*Birch, M.; Whiteley, K.;*

Software Engineering for Real Time Systems, 1989., Second International Conference on , 18-20 Sep 1989

Pages:21 - 25

[\[Abstract\]](#)   [\[PDF Full-Text \(432 KB\)\]](#)   **IEE CNF**

### 3 Setting break-points in distributed time-triggered architecture

*Smaili, I.; Ademaj, A.;*

High-Level Design Validation and Test Workshop, 2002. Seventh IEEE International , 27-29 Oct. 2002

**Pages:57 - 62**

[Abstract] [PDF Full-Text (672 KB)] IEEE CNF

#### 4 Breaking an application specific instruction-set processor: the first towards embedded software testing

*Dielissen, J.T.M.H.; Otero Mathijssen, B.L.; Huiskes, J.A.:*

High-Level Design Validation and Test Workshop, 2002. Seventh IEEE International , 27-29 Oct. 2002

Pages:89 - 92

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) IEEE CNF

### 5 **A dynamic debugger for asynchronous distributed algorithms**

*Sorel, P.E.; Fernandez, M.G.; Ghosh, S.;*  
Software, IEEE , Volume: 11 , Issue: 1 , Jan. 1994  
Pages:69 - 76

[\[Abstract\]](#) [\[PDF Full-Text \(964 KB\)\]](#) IEEE JNL

### 6 **Detecting nondeterminacy in parallel programs**

*Emrath, P.A.; Ghosh, S.; Padua, D.A.;*  
Software, IEEE , Volume: 9 , Issue: 1 , Jan. 1992  
Pages:69 - 77

[\[Abstract\]](#) [\[PDF Full-Text \(1376 KB\)\]](#) IEEE JNL

### 7 **Fully IEEE1149.1 compatible DFT solution for MIPS CPU CORE**

*Zhengyi Lu; Chengshou Sun;*  
ASIC, 2003. Proceedings. 5th International Conference on , Volume: 2 , 21-2 2003  
Pages:1140 - 1144 Vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(367 KB\)\]](#) IEEE CNF

### 8 **Debug facilities in the TriMedia CPU64 architecture**

*Vranken, H.;*  
Test Workshop 1999. Proceedings. European , 25-28 May 1999  
Pages:76 - 81

[\[Abstract\]](#) [\[PDF Full-Text \(96 KB\)\]](#) IEEE CNF

### 9 **Compiler optimization and its impact on development of real-time systems**

*Tucker, K.; Solomon, E.; Littlejohn, K.;*  
Digital Avionics Systems Conference, 1998. Proceedings., 17th DASC. The AIAA/IEEE/SAE , Volume: 1 , 31 Oct.-7 Nov. 1998  
Pages:C12/1 - C12/6 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(472 KB\)\]](#) IEEE CNF

### 10 **High-level CHILL debugging system in cross-development environ**

*YoungJoon Byun; Young-Sik Chung; Byung Sun Lee;*  
Parallel and Distributed Processing, 1998. PDP '98. Proceedings of the Sixth Euromicro Workshop on , 21-23 Jan. 1998  
Pages:211 - 216

[\[Abstract\]](#) [\[PDF Full-Text \(884 KB\)\]](#) IEEE CNF

### 11 **A caching compiler for C**

*Koehler, B.; Horspool, R.N.;*  
Communications, Computers, and Signal Processing, 1995. Proceedings. IEEE

Pacific Rim Conference on , 17-19 May 1995

Pages:141 - 144

[\[Abstract\]](#) [\[PDF Full-Text \(356 KB\)\]](#) IEEE CNF

---

12 **A proposed real-time ADA test**

*Newport, J.R.;*

Aerospace and Electronics Conference, 1994. NAECON 1994., Proceedings of IEEE 1994 National , 23-27 May 1994

Pages:932 - 936 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) IEEE CNF

---

13 **Software abort and multiprocessor debugging**

*Baek Youngsik; Jin Sungil;*

TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993

Pages:237 - 241 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) IEEE CNF

---

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

**IEEE Xplore**  
 RELEASE 1.8

 Welcome  
 United States Patent and Trademark Office


» Se

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

Your search matched **3** of **1058483** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

debug and stack

Search

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Identification of processing defects by focused ion beam (FIB) induced voltage contrast***Chun-Sheng Liu; Chih-Rong Chen; Yong-Fen Hsieh;*

Physical and Failure Analysis of Integrated Circuits, 1999. Proceedings of the 7th International Symposium on the , 5-9 July 1999

Pages:128 - 131

[\[Abstract\]](#)[\[PDF Full-Text \(572 KB\)\]](#)**IEEE CNF****2 Understanding and implementing computer network protocols through a lab project***El-Kharashi, M.W.; Darling, G.; Marykuca, B.; Shoja, G.C.;*

Education, IEEE Transactions on , Volume: 45 , Issue: 3 , Aug. 2002

Pages:276 - 284

[\[Abstract\]](#)[\[PDF Full-Text \(287 KB\)\]](#)**IEEE JNL****3 Automatic data visualization for novice Pascal programmers***Myers, B.A.; Chandhok, R.; Sareen, A.;*

Visual Languages, 1988., IEEE Workshop on , 10-12 Oct. 1988

Pages:192 - 198

[\[Abstract\]](#)[\[PDF Full-Text \(564 KB\)\]](#)**IEEE CNF**
[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved